

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 8-83) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	ATTY. DOCKET NO. BU9-97-149	SERIAL NO.
	APPLICANT Bronner et al.	
	FILING DATE	GROUP

JCS51 U.S. PTO
 09/105739
 06/26/98

U. S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
<i>SM</i>		5,670,806	9/1997	Jun			
<i>SM</i>		5,633,781	5/1997	Saenger et al.			
<i>SM</i>		5,291,053	3/1994	Pfiester et al.			
<i>SM</i>		5,146,291	9/1992	Watabe et al.			
<i>SM</i>		5,053,351	10/1991	Fazan et al.			

FOREIGN PATENT DOCUMENTS							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
<i>SM</i>	1	"The Evolution of IBM CMOS DRAM Technology" by Adler et al., IBM J. Res. Develop., Vol. 39 No. 1/2 January/March 1995, pp. 167-188
<i>SM</i>	2	"A 0.6 μm^2 256Mb Trench DRAM Cell With Self-Aligned BuriEd Strap (BEST)", by Nesbit et al., IEDM 93-627, pp. 26.2.1-26.2.4

EXAMINER <i>SM</i>	DATE CONSIDERED 12/2/98
-----------------------	----------------------------

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.